

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER:

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED 746(5) Q.A.

APPLICATION NO. 09/896059	CONT/PRIOR D	CLASS 716	SUBCLASS 18	ART UNIT 2825	EXAMINER Thompson
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APPLICANTS

Debashis Bhattacharya
Vamsi Boppana
Rabindra Roy
Jayanta Roy

TITLE

Method for automated design of integrated circuits with targeted quality objectives using dynamically generated building blocks

PTO-2040
12/89**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ 	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
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